

ID MOS Digital I.P. SHORTFORM

IM210: Programmable Divider

PRODUCT DESCRIPTION

The The IM210 enables to divide an input signal by integer N or values with decimal part which is multiple of 25. The two output signals are the same in case of division by an integer or an N.5 value (where N is an integer). In other cases, one pulse out of two is delayed.

When IM210 is used in PLL system, the output signals can be connected to two phase detectors. This enable to remove parasitic spectrum lines and to stabilize the VCO (Voltage Controlled Oscillator) in case of division by N.25 or N.75. The output signals have a high state of one clock period. The duty cycle is not 50%.

FEATURES

- Divider by step $\frac{1}{4}$
- 8 bits for the division value
- Internal generic structure, so the number of bits for the division value can be changed easily
- Two output signals

TYPICAL APPLICATIONS

- Frequency synthesizer
- PLL system

BLOCK DESCRIPTION

