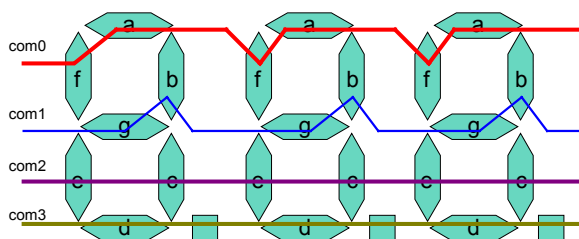


ID MOS Digital I.P. SHORTFORM

IM235: Low-level multiplexed LCD controller

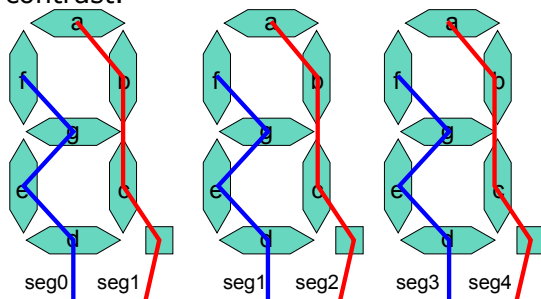
PRODUCT DESCRIPTION

To reduce the number of pins required both on the controller IC and the LCD display connector, a technique called time division or multiplexing is employed. This technique requires that the display is scanned, in our case, common by common, and the segments driven in the current common. The figure below illustrates a possible wiring of the commons across the display. For example, common 1 is red and connects all "f" and "a" segments together.



Common Interconnections

The segments are then driven directly as shown in the next figure, the resultant RMS voltage on each segment between the segment and the common determines the display contrast.



Segment Interconnections

The Low Level Multiplexed LCD is the digital part of the time division style LCD. The outputs for each common and segment drive need an analogue component to select voltages.

FEATURES

- 4 Commons outputs
- 30 Segments outputs
- More pixels can be added easily
- 1/4 Bias
- One CPU interface

BLOCK DESCRIPTION

