

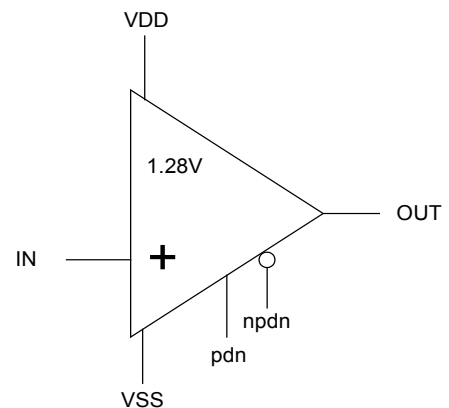
DATA SHEET

Typical Applications

→ voltages comparison

Features

- Technology CMOS
- Area : 0.21 mm² (463μm x 456 μm)
- Consumption : 150 μA typical
- Operating supply voltage : 4.5 V – 5.5 V
- Power down mode
- Low offset voltage
- Stable threshold 1.28V
- Hysteresis : 100 mV



Pin Name	Description
npdn	Power down control (active low).
pdn	Power down control (active high).
IN	input
OUT	output signal
VDD	High power supply
VSS	Low power supply

Product Description

This cell is a compare input voltage on IN pin with 1.28V very stable. The hysteresis of the comparator is equal to 100 mV.

A power down mode is available.

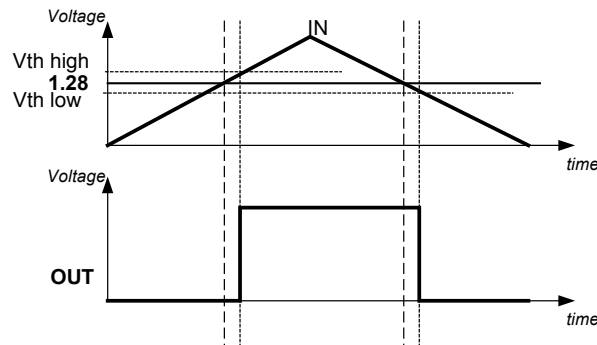
Absolute Maximum Ratings

Symbol	Parameter	Min	Typ	Max	Unit
VDD	Power supply voltage	4.5		5.5	V
Tjc	Junction temperature	-40		85	°C
Tst	Storage temperature	-65		150	°C

Characteristics

Symbol	Parameter	Min	Typ	Max	Unit
VDD	High power supply	4.5		5.5	V
VSS	Low power supply		0		V
IDD	Supply Current		150		μA
IDD_PD	Supply Current in Power Down Mode			0.1	μA
Hyst	Hysteresis		100		mV
Vth low	Low threshold	1.18		1.28	V
Vth high	High threshold	1.28		1.38	V

Waveform



Block diagram

