

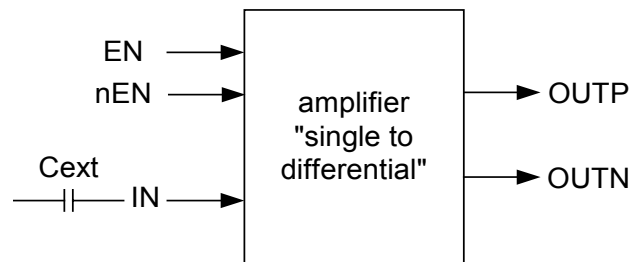
DATA SHEET

Typical Applications

- Amplification

Features

- Technology CMOS
- Area : 0.43 mm²
- Consumption : 5.4 mA maximum
- Operating supply voltage : 3 V – 5.5 V
- Power down mode
- Cut-off frequencies : 14kHz (Cext=1nF), 6MHz
- Gain : 3



Pin Name	Description
IN	input signal through external capacitor
OUTN	negative output
OUTP	positive output
EN	Power down mode input
nEN	inverted power down mode signal
VDD	High power supply
VSS	Low power supply

Product Description

This block filters and amplifies an input signal in order to generate 2 signals in phase opposition. The output signals are centered on Vref (2.5V).

Low cut-off frequency is adjusted by the external capacitor Cext.

A power down mode is available

Absolute Maximum Ratings

Symbol	Parameter	Min	Typ	Max	Unit
VDD	Power supply voltage	3	5	5.5	V
Tjc	Junction temperature	0	27	60	°C
Tst	Storage temperature	-65		150	°C

DC Characteristics

Symbol	Parameter	Min	Typ	Max	Unit
VDD	High power supply	3		5.5	V
VSS	Low power supply		0		V
IDD	Supply Current	3.4	4.1	5.4	mA
IDD_PD	Supply Current in power down mode			0.1	μA
Rin	Internal resistor		10		kOhms
Vref	Internal voltage reference	VDD/2 - 0.01	VDD/2	VDD/2 + 0.01	V

AC Characteristics

Symbol	Parameter	Min	Typ	Max	Unit
F _{low} OUTP	Low frequency OUTP (Cext=1nF)	10	13.9	18.6	kHz
F _{high} OUTP	High frequency OUTP	4.4	6.1	8.8	MHz
F _{low} OUTN	Low frequency OUTN (Cext=1nF)	10	13.9	18.6	kHz
F _{high} OUTN	High frequency OUTN	4.4	6.1	8.8	MHz
G1 _{1MHz}	Gain OUTP at 1MHz	2.77		2.9	
G1 _{2MHz}	Gain OUTP at 2MHz	2.54		2.85	
G2 _{1MHz}	Gain OUTN at 1MHz	2.79		2.9	
G2 _{2MHz}	Gain OUTN at 2MHz	2.54		2.85	

Block diagram / Waveform

