

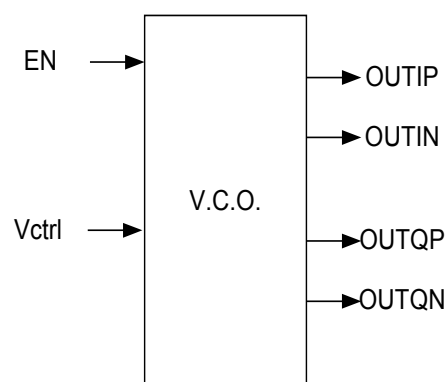
DATA SHEET

Typical Applications

- PLL
- frequency synthesizer

Features

- Technology BiCMOS
- Area : 0.38 mm² (850μm x 450μm)
- Consumption : 3 mA typical
- Operating supply voltage : 2.7 V – 3.3 V
- Power down mode
- Low phase noise
- fully integrated
- differential outputs



Pin Name	Description
EN	Enable (active high).
Vctrl	frequency control input voltage
OUTIP	positive output I
OUTIN	negative output I
OUTQP	positive output Q
OUTQN	negative output Q
VDD	High power supply
VSS	Low power supply

Product Description

The V.C.O generates an output frequency proportional to the input voltage.

A power down mode is available.

It is fully integrated and has a low phase noise.

The outputs I and Q are 90° out of phase.

Absolute Maximum Ratings

Symbol	Parameter	Min	Typ	Max	Unit
VDD	Power supply voltage	2.7		3.3	V
Tjc	Junction temperature	-20		85	°C
Tst	Storage temperature	-65		150	°C

Characteristics

Symbol	Parameter	Min	Typ	Max	Unit
VDD	High power supply	2.7		3.3	V
VSS	Low power supply		0		V
IDD	Supply Current		3		mA
IDD_PD	Supply Current in Power Down Mode			100	nA
Sl	slope		330		MHz/V
Nph	Phase noise at 100kHz with carrier frequency 433MHz		-80		dB _c /Hz
Freq	Frequency range	400		500	MHz

Waveform

