

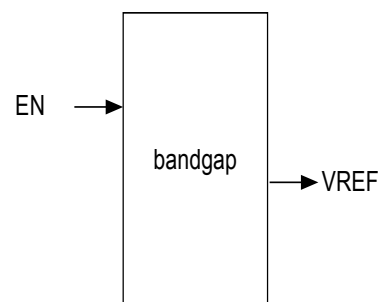
DATA SHEET

Typical Applications

- stable voltage generation

Features

- Technology CMOS
- Area : 0.063 mm² (379μm x 167μm)
- Consumption : 28 μA max
- Operating supply voltage : 4.5 V – 5.5 V
- Power down mode
- Output voltage 1.25V



Pin Name	Description
EN	Power down control (active high).
VREF	output reference voltage 1.25V
VDD	High power supply
VSS	Low power supply

Product Description

The bandgap generates a stable output voltage 1.25V

A power down mode is available

Absolute Maximum Ratings

Symbol	Parameter	Min	Typ	Max	Unit
VDD	Power supply voltage	4.5		5.5	V
T _{jc}	Junction temperature	-40		80	°C
T _{st}	Storage temperature	-65		150	°C

DC Characteristics

Symbol	Parameter	Min	Typ	Max	Unit
VDD	High power supply	4.5		5.5	V
VSS	Low power supply		0		V
VREF	VREF output voltage	1.21	1.25	1.29	V
IDD	Supply Current	15	23	28	μA
IDD_PD	Supply Current in power down mode			0.01	μA

AC Characteristics

Symbol	Parameter	Min	Typ	Max	Unit
T _{st}	Startup time		37	100	μs