

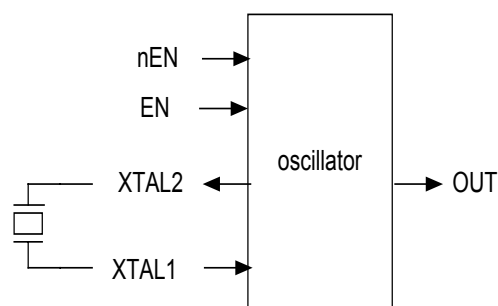
DATA SHEET

Typical Applications

→ Application including digital logic.

Features

- Technology CMOS
- Area : 0.32 mm² (1200μm x 260μm)
- Consumption : 0.8 mA max (40MHz, 5.5V)
- Operating supply voltage : 3 V – 5.5 V
- Power down mode
- No external capacitors required
- Frequency range : 1MHz to 40MHz
- Layout including pads



| Pin Name | Description |
|--------------|-----------------------------------|
| nEN | Power down control (active low). |
| EN | Power down control (active high). |
| XTAL1 | Crystal terminal |
| XTAL2 | Crystal terminal |
| OUT | output clock |
| VDD | High power supply |
| VSS | Low power supply |

| EN | nEN | XTAL1 | XTAL2 | OUT |
|----|-----|-------|-------|-----|
| 1 | 0 | X | 0 | 0 |
| 0 | 1 | 1 | 0 | 1 |
| 0 | 1 | 0 | 1 | 0 |

Product Description

XTAL1 and XTAL2 pins may be used in either of 2 modes to generate output clock OUT. In mode 1, a crystal is connected between them. In mode 2, XTAL1 is left floating and a capacitor is connected between XTAL2 and a clock generator.

Absolute Maximum Ratings

| Symbol | Parameter | Min | Typ | Max | Unit |
|-----------------|----------------------|-----|-----|-----|------|
| VDD | Power supply voltage | 3 | | 5.5 | V |
| T _{jc} | Junction temperature | 0 | | 60 | °C |
| T _{st} | Storage temperature | -65 | | 150 | °C |

Characteristics

| Symbol | Parameter | Min | Typ | Max | Unit |
|--------|-----------------------------------|------|-----|------|------|
| VDD | High power supply | 3 | | 5.5 | V |
| VSS | Low power supply | | 0 | | V |
| IDD_16 | Supply Current at 16MHz | 0.25 | | 0.54 | μA |
| IDD_40 | Supply Current at 40 MHz | 0.38 | | 0.8 | mA |
| IDD_PD | Supply Current in Power Down Mode | | | 0.1 | μA |